



LifeView solution enables PC viewing of TV programs

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LifeView Inc. has over the past 13 years become a leader in the image processing industry, introducing innovative solutions such as TV/video capture cards, boxes and multimedia applications.

The company's flagship product is the TV/video tuner card under the FlyVideo series. Given the installation rate of tuner cards in PCs is just seven to eight percent, the technology still has room to grow, said Gray Chen, general manager.

Generally speaking, tuner cards convert TV signals into formats readable to the computer, thus allowing users to watch TV programs on their PCs. One issue facing distributors has been the different broadcasting formats used throughout the world. Taiwan, Japan

and the United States use the NTSC format, while European countries use PAL.

Distributors therefore have to stock up on cards supporting each format and are subject to high inventory costs.

LifeView's R&D team has successfully developed a solution, a silicon tuner capable of reading all the different broadcasting formats used around the world, saving systems integrators inventory costs in the process. Installation of the card in a laptop computer allows the viewing of local TV stations on the notebook no matter where the user is, Chen said.

The solution also allows the recording of a particular TV program that can be stored in the hard drive for later viewing or burning onto DVD-ROMs, he added.

The add-on version of the technology, the FlyTV TV/PM video capture box,



sends data into the PC through the USB port. All LifeView products are on display at COMPUTEX TAIPEI 2003.

gies of programmable logic with
al-
The dual bank Flash architec-
programmable decode logic support
while-WRITE concurrent access.
shown as In-Application Program-
Memory mapping is handled by
d decoding PLD that can assign
SRAM memory segment to any
ary memory page, or bank. Addi-
sh memory can be allocated to
pace or data space in almost any
as needed, which is a feature
a PSD devices.

ipherals include six PWM chan-
master/slave bus controller; two
ARTs, supervisory functions such
log timer and low-voltage detect.

general-purpose I/O pins.
s can use the 16-macrocell CPLD
internal glue logic devices such as
s, 20L8 PALs, and 74-series dis-
ICs. Common functions forged
PLD array include state machines,
ed counters, keypad and control
faces, chip-selects for external de-
dividers, multiplexers and hand-
circuits.

OO devices are sampling now with
duction planned for late 4Q 2003
nd 80-pin TQFP packages, with in-
erating temperature -40C to +85C
d 3.3V.